

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claims 1-10 (Canceled).

Claim 11 (Currently amended): The method according to claim 18, wherein ~~the~~ a period duration, ~~or a frequency, respectively,~~ for the pulse width modulation for switching over the switching elements of the bridge inverter is set as a function of the current detected.

Claim 12 (Previously presented): The method according to claim 18, wherein the switching times of the switching elements of the bridge inverter are evaluated as a function of the current detected and set automatically.

Claims 13-14 (Canceled).

Claim 15 (Previously presented): The method according to claim 18, wherein the switching elements are activated at appropriately set points of time.

Claim 16 (Currently amended): A solar inverter for feeding current produced by a d.c. voltage source into an a.c. voltage grid, said d.c. voltage source being formed by a solar module, said inverter comprising a bridge inverter, a transformer having a primary winding, a rectifier, a buck chopper including a full bridge and an output filter<sub>7i</sub>

wherein a control device ~~being~~ is provided for controlling the parameters of the inverter<sub>7i</sub>

wherein a device for detecting the current produced by the d.c. voltage source and flowing over the primary winding of the transformer is provided, which device is connected to the control device<sub>7i</sub> and

wherein the bridge inverter is designed for adapting a dead time for the switching elements ~~and/or a pulse duration, or frequency, respectively, for the pulse width modulation~~ as a function of the mean value of the current detected flowing over the primary winding of the transformer by selecting the dead time from a table with correspondingly stored data for the most varying mean values, the dead time representing a time of the switching elements for switching over from one switching element to a further switching element connected in series of the bridge inverter, thereby ensuring that parasitic ~~capacities~~ capacitances stored in the switching elements of the bridge inverter can be

completely recharged and no excessively long switching pauses can occur at the same time.

Claim 17 (Canceled).

Claim 18 (Currently amended): A method for a solar inverter for feeding current produced by a d.c. voltage source formed by a solar module into an a.c. voltage grid comprising the steps of:

(a) chopping the current produced by the d.c. voltage source in a form of a pulse width modulation by a bridge inverter by alternate switching of switching elements connected in parallel and connected in series;

(b) transmitting the current chopped via a transformer connected between the switching elements that are connected in series, the transformer having a primary winding; and

(c) rectifying the current transmitted and feeding the current into the a.c. voltage grid via a buck chopper;

wherein, for a power adaptation, the switching times of the switching elements of the bridge inverter are controlled, or regulated, ~~respectively~~;

wherein the current produced by the d.c. voltage source, is detected from current flowing over the primary winding of the

transformer continuously or at cyclical intervals which are  
~~cyclical or is detected permanently~~, and

wherein a dead time of the switching elements of the bridge inverter is set as a function of the mean value of the detected current of the d.c. voltage source flowing over the primary winding of the transformer by selecting the dead time from a table with correspondingly stored data for the most varying mean values, the dead time representing a time of the switching elements for switching over from one switching element to a further switching element connected in series of the bridge inverter, thereby ensuring that parasitic ~~capacities~~ capacitances stored in the switching elements of the bridge inverter can be completely recharged and no excessively long switching pauses can occur at the same time.